

PATENT NUMBER AND
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10006334	FILING DATE 12/03/2001	CLASS 438	SUBCLASS 674	GAU 2818	EXAMINER Q. Hoang
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**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A CIP OF 09/430,348 10/29/1999 PAT 6,329,287

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input checked="" type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
Verified and Acknowledged Examiners's initials		NSC1-G0610 [P04402 P01]	
TITLE : Method for manufacturing an integrated circuit structure with limited source salicidation U.S. DEPT. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)			

DAYS OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims 02	Print Ch. No. 02
Assistant Examiner		DRAWING	
		Sheets Drawg.	Fig. Drawg.
Primary Examiner		Print Fig.	
PREPARED FOR ISSUE		Application Examiner	
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